

Trench IGBT Modules

SKM75GB07E3

Target Data

Features*

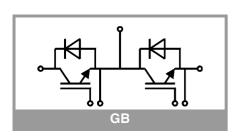
- V_{CE(sat)} with positive temperature coefficient
- High short circuit capability, self limiting to 6 x I_{cnom}
- Fast & soft switching inverse CAL diodes
- Insulated copper baseplate using DCB Technology (Direct Copper Bonding)
- · With integrated gate resistor

Typical Applications

- AC inverter drives
- UPS
- · Electronic welders
- · Wind power
- · Public transport

Remarks

- Case temperature limited to T_c = 125°C max.
- Recommended T_{op} = -40 ... +150°C
- Product reliability results valid for T_i = 150°C
- Use of soft R_G necessary



Absolute	Maximum Rati	ngs		
Symbol	Conditions		Values	Unit
IGBT	•			
V_{CES}	T _j = 25 °C		650	V
I _C	T _j = 175 °C	T _c = 25 °C	99	Α
		T _c = 80 °C	74	Α
I _{Cnom}			75	Α
I _{CRM}			225	Α
V_{GES}			-20 20	V
t _{psc}	$V_{CC} = 360 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 650 \text{ V}$	T _j = 150 °C	6	μs
Tj			-40 175	°C
Inverse d	liode			
V_{RRM}	T _j = 25 °C		650	V
I _F	T _j = 175 °C	T _c = 25 °C	84	Α
		T _c = 80 °C	62	Α
I _{FRM}			100	Α
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		550	Α
Tj			-40 175	°C
Module				
I _{t(RMS)}			200	Α
T _{stg}	module without TIM		-40 125	°C
V _{isol}	AC sinus 50 Hz	, t = 1 min	4000	V

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
IGBT			•				
$V_{CE(sat)}$ $I_{C} = 75 \text{ A}$ $V_{GE} = 15 \text{ N}$ chiplevel	•	T _j = 25 °C		1.45	1.77	V	
		T _j = 150 °C		1.72	2.10	V	
V _{CE0}	chiplevel	$T_j = 25 ^{\circ}C$		0.90	1.00	V	
		T _j = 150 °C		0.82	0.90	V	
r _{CE}	$V_{GE} = 15 \text{ V}$	$T_j = 25 ^{\circ}C$		7.3	10	$m\Omega$	
	chiplevel	T _j = 150 °C		12	16	$m\Omega$	
$V_{GE(th)}$	$V_{GE}=V_{CE}$, $I_{C}=1.2$ mA		5.1	5.8	6.4	V	
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 6$	50 V, T _j = 25 °C				mA	
C _{ies}	V - 25 V	f = 1 MHz		4.6		nF	
Coes	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		0.30		nF	
C _{res}		f = 1 MHz		0.14		nF	
Q_G	V _{GE} = - 8 V+ 15 V			680		nC	
R_{Gint}	T _j = 25 °C			4.0		Ω	
$t_{d(on)}$	$V_{CC} = 300 \text{ V}$	T _j = 150 °C		t.b.d.		ns	
t _r	$I_C = 75 \text{ A}$ $V_{GE} = +15/-15 \text{ V}$	T _j = 150 °C		t.b.d.		ns	
E _{on}	$R_{G \text{ off}} = 15/15 \text{ V}$ $R_{G \text{ off}} = 2.7 \Omega$ $R_{G \text{ off}} = 15 \Omega$	T _j = 150 °C		2.4		mJ	
$t_{d(off)}$		T _j = 150 °C		t.b.d.		ns	
t _f		T _j = 150 °C		t.b.d.		ns	
E _{off}		T _j = 150 °C		3.1		mJ	
R _{th(j-c)}	per IGBT				0.591	K/W	
R _{th(c-s)}	per IGBT (λ _{grease} =0.81 W/(m*K))			0.064		K/W	
R _{th(c-s)}	per IGBT, pre-applied phase change material			0.054		K/W	



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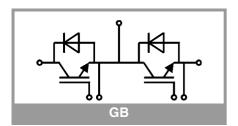
Typical Applications

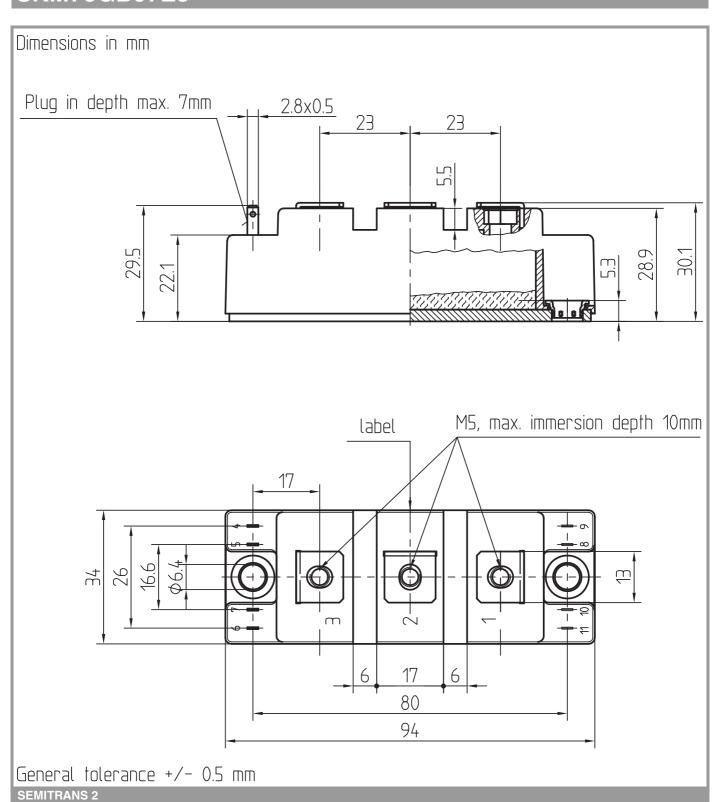
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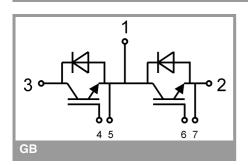
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- Recommended T_{op} = -40 ... +150°C
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Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
Inverse di	ode						
$V_F = V_{EC}$	I _F = 50 A	T _j = 25 °C		1.37	1.73	V	
	V _{GE} = 0 V chiplevel	T _j = 150 °C		1.35	1.72	V	
V_{F0}	chiplevel	T _j = 25 °C		1.04	1.24	V	
	Chipievei	T _j = 150 °C		0.85	0.99	V	
r _F	chiplevel	T _j = 25 °C		6.7	9.8	mΩ	
	Chipievei	T _j = 150 °C		10.0	15	mΩ	
I _{RRM}	I _F = 50 A	T _j = 150 °C		t.b.d.		Α	
Q _{rr}	V _{GE} = -7.5 V	T _j = 150 °C		t.b.d.		μC	
E _{rr}	$V_{GE} = -7.5 \text{ V}$ $V_{CC} = 300 \text{ V}$	T _j = 150 °C		1.24		mJ	
R _{th(j-c)}	per diode			0.85	K/W		
R _{th(c-s)}	per diode (λ _{grease} =0		t.b.d.		K/W		
R _{th(c-s)}	per diode, pre-applied phase change material			t.b.d.		K/W	
Module							
L _{CE}				30		nΗ	
R _{CC'+EE'}	measured per	T _C = 25 °C		0.65		mΩ	
	switch	T _C = 125 °C		1.09		mΩ	
R _{th(c-s)1}	calculated without thermal coupling			t.b.d.		K/W	
R _{th(c-s)2}	including thermal corporation T_s underneath mod $(\lambda_{grease}=0.81 \text{ W/(m}^3))$		t.b.d.		K/W		
R _{th(c-s)2}	including thermal coupling, T _s underneath module, pre-applied phase change material			-		K/W	
Ms	to heat sink M6	3		5	Nm		
Mt		to terminals M5	2.5		5	Nm	
						Nm	
W					160	g	







This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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