

IGBT Modules

Power Module (V series) 1200V / 200A / 2-in-1 package

■ Features

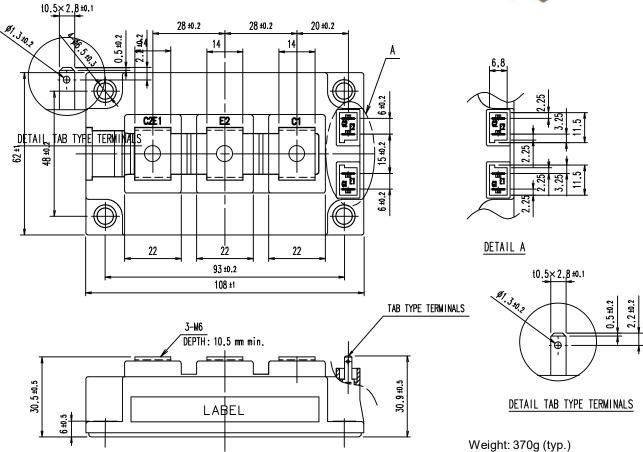
AC-switch
High speed switching
Voltage drive
Low Inductance module structure

■ Applications

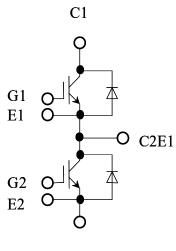
AC-switch for UPS,PCS and etc.

■ Outline drawing (Unit:mm)





■ Equivalent circuit



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■ Absolute maximum ratings (at T_C= 25°C unless otherwise specified)

Items		Symbols	Conditions		Maximum ratings	Units
Collector-Emitter voltage		V _{CES}			1200	V
Gate-Emitte	r voltage	V _{GES}			±20	V
		1	Continuous	T _C =100°C	200	
		Ic	Continuous	<i>T</i> _C =25°C	240	
Collector cu	rrent	I _C pulse	1ms		400	Α
					200	
		-I _C pulse	1ms		400	
Collector power dissipation		Pc	1 device		1110	W
Junction temperature		$T_{\rm j}$			175	
Operating junction temperature		T_{jop}			150	
(under switching conditions)					100	°C
Case temperature		T _c			125	
Storage temperature		${T}_{ m stg}$			-40 ~ 125	
Isolation	Between terminal and copper base	V_{iso}	AC: 1min.		4000	VAC
voltage	(*1)	v iso	AO. IIIIII.		4000	٧٨٥
Screw	Mounting	-	M5 or M6		3.0~6.0	N m
torque	Terminals	-	M6		2.5~5.0	INIII

^(*1) All terminals should be connected together during the test.

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■ Electrical characteristics (at T_j= 25°C unless otherwise specified)

NOTICE:

The external gate resistance (R_g) shown below is one of our recommend value for the purpose of minimum switching loss. However the optimum R_g depends on circuit configuration and/or environment. We recommend that the R_g has to be carefully chosen based on consideration if IGBT module matches design criteria, for example, switching loss, EMC/EMI, spike voltage, surge current and no unexpected oscillation and so on.

Items	Symbols	Conditio	ne	Ch	aracterist	ics	Units	
items	Syllibols	Conditio	1115	min.	typ.	max.	UIIIIS	
Zero gate voltage collector current	I _{CES}	V _{GE} =0V, V _{CE} =1200V		-	-	2.0	mA	
Gate-Emitter leakage current	I _{GES}	V _{CE} =0V,V _{GE} =±20V		-	ı	400	nA	
Gate-Emitter threshold voltage	$V_{GE(th)}$	V _{CE} =20V,I _C =200mA		6.0	6.5	7.0	V	
	V _{CE(sat)}		T _j =25°C	-	1.95	2.40		
		V_{GE} =15V, I_{C} =200A	T _j =125°C	-	2.25	-		
Collector-Emitter	(terminal)		T _j =150°C	-	2.30	-	V	
saturation voltage	V		T _j =25°C	-	1.75	2.15	V	
	V _{CE(sat)}	$V_{\rm GE}$ =15V, $I_{\rm C}$ =200A	T _j =125°C	-	2.05	-		
	(chip)		T _j =150°C	-	2.10	-		
Internal gate resistance	R _{g(int)}	-		-	3.8	-	Ω	
Input capacitance	C ies	V_{CE} =10V, V_{GE} =0V, f =1MHz		-	18.2	-	nF	
	t on			-	600	-		
Turn-on time	t _r	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		-	200	-	nsec	
	t _{r(i)}			-	50	-		
Turn-off time	t off			800	-	1		
Turn-on time	t _f			-	80	-	1	
	V _F		T _j =25°C	-	1.85	2.30		
Forward on voltage		$V_{GE} = 0V, I_{F} = 200A$	T _i =125°C	-	2.00	-		
	(terminal)		T _i =150°C	-	1.95	-	V	
	V _F		T _i =25°C	-	1.70	2.15	V	
		$V_{GE} = 0V, I_{F} = 200A$	T _j =125°C	-	1.85	-		
	(chip)		T _j =150°C	-	1.80	-		
Reverse recovery time	t _{rr}	I _F =200A		-	150	-	nsec	

■ Thermal resistance characteristics

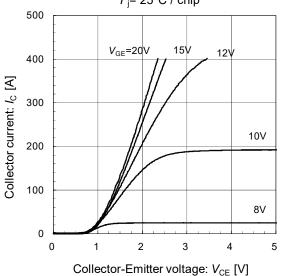
Items	Symbols	Conditions	Characteristics			Units
items	Syllibols	Conditions	min. typ.		max.	UIIIIS
Thermal resistance	D	IGBT	-	-	0.135	
(1device)	R _{th(j-c)}	FWD	-	-	0.200	°C/W
Contact thermal resistance	D	with thermal compound		0.025		C/VV
(1device) (*1)	R _{th(c-f)}	with thermal compound	-	0.025	-	

^(*1) This is the value which is defined mounting on the additional cooling fin with thermal compound.

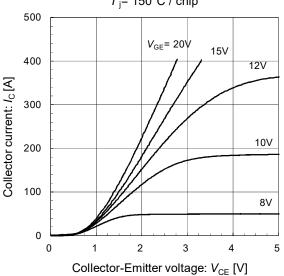


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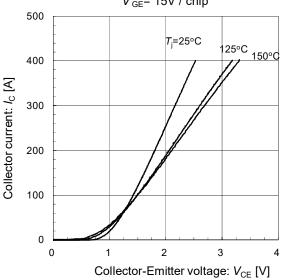
Collector current vs. Collector-Emitter voltage (typ.) T_i = 25°C / chip



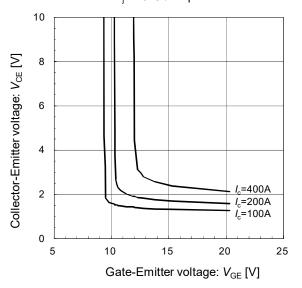
Collector current vs. Collector-Emitter voltage (typ.) T_i = 150°C / chip



Collector current vs. Collector-Emitter voltage (typ.) V_{GE} = 15V / chip

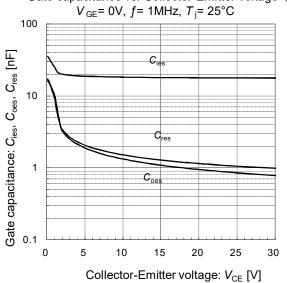


Collector-Emitter voltage vs. Gate-Emitter voltage (typ.) T_i = 25°C / chip



Dynamic gate charge (typ.)

Gate capacitance vs. Collector-Emitter voltage (typ.) $V_{r=0} = 0 V_r = 1 \text{MHz} \quad T = 25^{\circ}\text{C}$



 $V_{\rm CC}$ =600V, $I_{\rm C}$ =200A, $T_{\rm i}$ =25°C 800 20 600 15 Collector-Emitter voltage: V_{CE} [V] Gate-Emitter voltage: V_{GE} [V] $\stackrel{\cdot}{_{1}}$ 0 $\stackrel{\cdot}{_{2}}$ 0 $\stackrel{\cdot}{_{3}}$ $V_{\sf CE}$ 400 200 0 -200 $V_{\rm GE}$ -400 -600 -20 -800

Gate charge: Qq [nC]

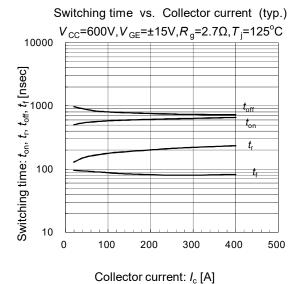
2000

-2000

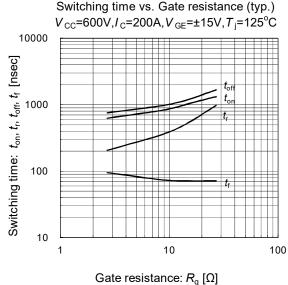
-1000



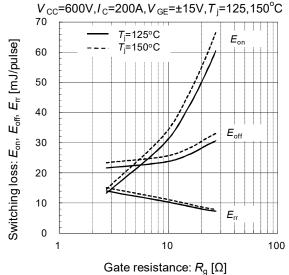
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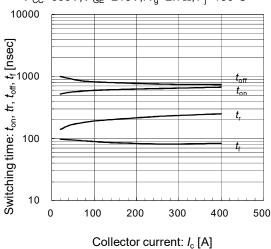




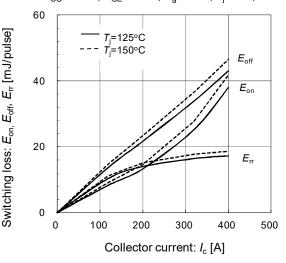
Switching loss vs. Gate resistance (typ.)



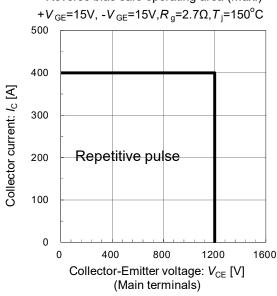
Switching time vs. Collector current (typ.) V_{CC} =600V, V_{GE} =±15V, R_g =2.7 Ω , T_i =150°C



Switching loss vs. Collector current (typ.) $V_{CC}=600V$, $V_{GE}=\pm15V$, $R_q=2.7\Omega$, $T_i=125$, 150° C

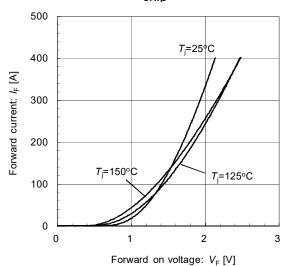


Reverse bias safe operating area (max.)

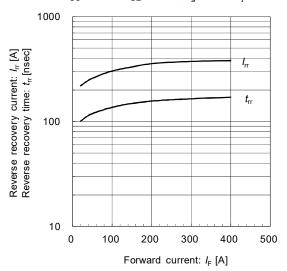


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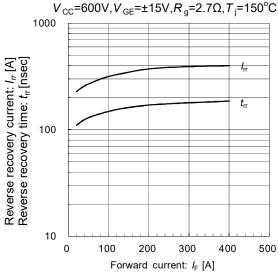
Forward current vs. Forward voltage (typ.) chip

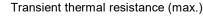


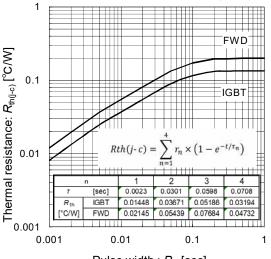
Reverse recovery characteristics (typ.) V_{CC} =600V, V_{GE} =±15V, R_q =2.7 Ω , T_i =125°C



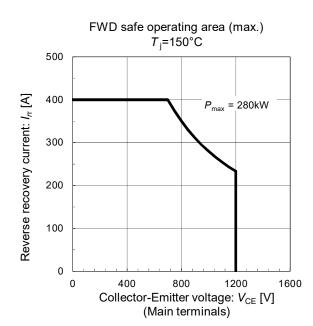
Reverse recovery characteristics (typ.)







Pulse width : Pw [sec]





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